

The schematic diagram illustrates the power supply circuit for the TMS320C49 DSP. It shows the connection between the DSP's VDD, GND, and VDDIO pins and external components. A 10µF electrolytic capacitor is connected to the VDD pin, while a 0.1µF ceramic capacitor is connected to the GND pin. The VDDIO pin is connected to a 3.3V regulator. The ground connections are labeled GND and GND1.